## GURU TEGH BAHADUR INSTITUTE OF TECHNOLOGY

## (G-8 Area Rajouri Garden New Delhi – 110064)

## LECTURE PLAN FOR ANALOG ELECTRONICS

S.NO.	TOPIC TO BE COVERED	No. of
		lecturers
		alloted
1-	Introduction, general characteristics, energy levels	2
2-	Extrinsic materials n & p type ,Ideal diode ,basic construction & $\frac{1}{2}$	3
2	Equivalent singuit drift & diffusion summents transition & diffusion	2
5-	capacitance, Reverse recovery times, temperatures effect, diode spe-	5
4	Different types of diades gener versator schettly, newer	2
4-	Different types of diodes, zener, varactor, schottky, power,	3
	tunnel, photodiode & LED, Switch mode power supply.	
5-	Half wave rectifier & full wave rectifier, bridge rectifier	2
	BIPOLAR JUNCTION TRANSISTOR	
6-	Introduction, construction, B.J.T operation	2
7-	B.J.T characteristics ,load line ,operating point ,leakage currents	3
	saturation & cut off mode of operations, Ebers molls model	
	2 <sup>nd</sup> Term	
	BIAS STABLIZATION	
8-	Need for stabilization, fixed bias, self bias, emitter bias	3
9-	Bias stability with respect of variation in Ico, Vbe, B,	3
	stablisation factor& thermal stability.	
	SMALL SIGNAL AMPLIFIERS	
10-	Analysis of (CB,CE,CC)	2
11-	Hybrid modle for transistor at low frequency, RC coupled amp, Mid band model, gain and impedance.	2
12-	Comparisons of different configurations, Darlington pair	2
	MULTISTAGE AMPLIFIER	
13-	Cascaded amplifiers. Calculations of gain impedance and	3
	bandwidth, design of multistage amplifiers.	
	3 <sup>rd</sup> Term	
	FEEDBACK AMLIFIERS	
14	Feedback concepts, classification of feedback amplifiers,	2
	properties of negative recuback amplifiers, impedance	

	consideration in different configurations, examples of analysis of feedback amplifiers.	
	FIELD EFFECT TRANSISTORS	
15	Introduction to FET, classification and characteristics, operating point, biasing	2
16	Depletion and enhancement MOSFETS	1

Lecturer :

AMRISH KUMAR